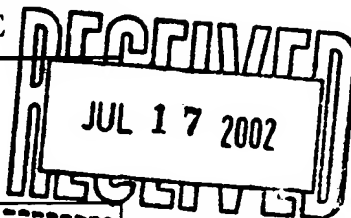




2182  
#2  
ID  
7/17/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT



Application No.: 10/061,695

Filed: February 1, 2002

Inventor(s):

Sribalan Santhanam

Vincent R. von Kaenel

David A. Kruckemyer

Examiner: Unknown

Group/Art Unit: 2182

Atty. Dkt. No: 5580-04900

Title: **TWO LEVEL CLOCK  
GATING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on the date indicated below.

Lawrence J. Merkel

Name of Registered Representative

*[Signature]*

July 1, 2002

Signature

Date

RECEIVED

INFORMATION DISCLOSURE STATEMENT

JUL 10 2002

Commissioner for Patents  
Washington, D.C. 20231

Technology Center 2100

Sir:

Applicant requests consideration of ☒ the references listed on the attached Form PTO-1449 and/or ☒ the additional information identified below in paragraph 3. A copy of each reference listed on the Form PTO-1449 is enclosed.

1. This Information Disclosure Statement is submitted:

- a. ☐ within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);  
☐ within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;  
☒ before the mailing date of a first Office Action on the merits; or  
☐ before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.
- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 is enclosed.
- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.

2. It is hereby certified:

- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart

foreign application not more than three months prior to the filing of the Statement, or

☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.

3. ☒ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

U.S. Patent Application Serial No. 10/127,103 (5580-05100)  
U.S. Patent Application Serial No. 10/136,109 (5580-04501)  
U.S. Patent Application Serial No. 10/127,106 (5580-05400)  
U.S. Patent Application Serial No. 10/127,513 (5580-05500)  
U.S. Patent Application Serial No. 10/127,259 (5580-05600)  
pp. 1-2 entitled "Background of the Invention" from the specification of the captioned application.

4. For each non-English language reference listed on the attached Form PTO-1449:

☐ reference is made to an English language translation submitted herewith, and/or

☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or

☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or

☐ reference is made to the concise explanation contained in the specification of the present application at page(s) \_\_\_\_\_, and/or

☐ reference is made to the concise explanation set forth below:

5. ☐ Applicant also offers the following comments for the Examiner's consideration:

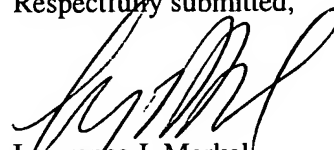
6. ☐ Also enclosed is a copy of a foreign search report citing these references.

7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.

8. ☐ Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

If any required fees are missing, the Commissioner is authorized to charge said fees to Conley, Rose & Tayon, P.C. Deposit Account No. 50-1505/5580-04900/LJM.

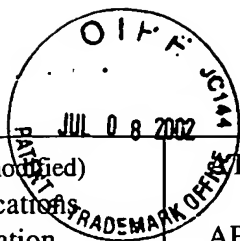
Respectfully submitted,



Lawrence J. Merkel  
Reg. No. 41,191  
Agent for Applicant(s)

CONLEY, ROSE & TAYON, P.C.  
P. O. Box 398  
Austin, Texas 78767  
(512) 476-1400

Date: July 1, 2002

**Form PTO-1449** (modified)

List of Patents and Publications  
For Applicant's Information  
Disclosure Statement  
(Use several sheets if necessary)

PRIORITY. DKT. NO. 5580-04900

APPLICANT: Santhanam, et al.

FILING DATE: February 1, 2002

SERIAL NO. 10/061,695

GROUP: 2182

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB	FILING DATE IF APPROPRIATE
	A13	5,459,736	10/17/95	Nakamura			
	A14	5,689,517	11/18/97	Ruparel			
	A15	6,318,911	11/20/01	Kitahara			
	A16	6,247,134	6/12/01	Sproch, et al.			
	A17	6,272,667	8/7/01	Minami, et al.			
	A18	5,815,725	9/29/98	Feierbach			
	A19	4,061,933	12/6/77	Schroeder, et al.			
	A20	5,831,462	11/3/98	Witt, et al.			
	A21	6,223,282	4/24/01	Kang			
	A22	6,411,152	6/25/02	Dobberpuhl			

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

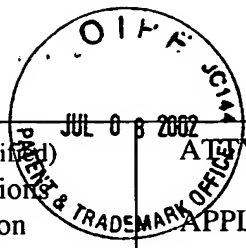
A1	SiByte, "Target Applications," <a href="http://sibyte.com/mercurian/applications.htm">http://sibyte.com/mercurian/applications.htm</a> , January 15, 2001, 2 pages.
A2	SiByte, "SiByte Technology," <a href="http://sibyte.com/mercurian/technology.htm">http://sibyte.com/mercurian/technology.htm</a> , January 15, 2001, 3 pages.
A3	SiByte, "The Mercurian Processor," <a href="http://sibyte.com/mercurian">http://sibyte.com/mercurian</a> , January 15, 2001, 2 pages.
A4	SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.
A5	SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.
A6	Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.
A7	Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.
A8	Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPUs; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, June 2000, Microprocessor Report, 4 pages.
A9	SiByte, Letter from Anu Sundaresan, May 18, 2000, 1 page.
A10	Stephany, et al., "FP 15.5: A 200 MHz 32b 0.5W CMOS RISC Microprocessor," Digital Semiconductor, Austin, TX, IEEE, 1998, pages 15.5-1 to 15.5-9.
A11	Santhanam, et al., "SA 18.6: A Low-Cost 300MHz RISC CPU with Attached Media Processor," Digital Equipment Corp., Palo Alto, CA, IEEE, 1998, pages 18.6-1 to 18.6-9.
A12	Montanaro, et al., "A 160-MHz, 32-b, 0.5-W CMOS RISC Microprocessor," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, 12 pages.

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

**Form PTO-1449** (modified)

List of Patents and Publications

For Applicant's Information

Disclosure Statement

(Use several sheets if necessary)

ATTY. DKT. NO. 5580-04900

APPLICANT: Santhanam, et al.

FILING DATE: February 1, 2002

SERIAL NO. 10/061,695

GROUP: 2182

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

**RECEIVED**  
**JUL 10 2002**  
 Technology Center 2100

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
	A23	522 413	1/13/93	EP			

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

	A24	Weiss, et al., "The On-Chip 3MB Subarray Based 3 <sup>rd</sup> Level Cache on an Itanium Microprocessor," IEEE, ISSCC 2002, Session 6, 3 pages.
	A25	Josephson, et al., "Test Methodology for the McKinley Processor," IEEE, ITC International Test Conference, Paper 21.1., 2001, pp. 578-585.
	A26	Santhanam, et al., Presentation for: "A 1GHz Power Efficient Single Chip Multiprocessor System For Broadband Networking Applications," Broadcom Corporation, June 14, 2001, 19 pages.
	A27	Santanam, et al. "A 1GHz Power Efficient Single Chip Multiprocessor System For Broadband Networking Applications," Broadcom Corporation, June 14, 2001, 4 pages.

EXAMINER:

DATE CONSIDERED:

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